	8	7		6		5	Ļ	4		3	
INSTRUCTIONS:											
D	<ol> <li>ALLOW INTERN</li> <li>OPEN DOOR P</li> <li>USING A VOLTI</li> <li>LOCATE FILTER</li> <li>REMOVE WIRE</li> <li>REMOVE #8 SE</li> <li>PLACE SPARE O</li> <li>REPLACE #8 SE</li> <li>CONFIRM POA</li> <li>REPLACE WIRE</li> </ol>	EN) AC INPUT CIRCUIT BREAKE IAL VOLTAGES TO DISSIPATE. PANEL, AND CAREFULLY REMO METER, VERIFY ALL POTENTIAL CAPACITOR ASSEMBLY (C1x, #20 AND #21 FROM LOWER ELF TAPPING HARDWARE FAST CAP ASSEMBLY ON CHASSIS ( ELF TAPPING HARDWARE. ARITY (+/-) OF CAPS BEFORE P #20 AND WIRE #21 AS SHOW R EJ5311-01, CONNECT WIRES	DVE PLEXIGLASS SHIELDS VOLTAGES ARE AT "0". /R9) ON REAR LEFT OF M CAPACITOR TERMINALS IENING ASSEMBLY TO CH CENTERING SLOTS AROU PROCEEDING. VN BELOW.	S. MOUNTING BASE. S. HASSIS. IND MOUNTING HOL	.ES.	) terminals as de	EPICTED BELC	w.			FO
С		R9 NOTE NEG		ORIENTATIO	N CRITICAL				e e	a	0
+		WIF #2	RE REPLACEMENT K PART NUMBERS: EJ5311-01.					(		0	0
В		Ν		ORIENTATION	I CRITICAL					0	
Α		WIRE #21	FRONT VIEW REPLACEMENT K PART NUMBERS EJ5311-02. EJ5311-03. EJ5311-08.				-	REV. DRN BY CHK BY / OESCRIPTION Initial Release	3/28/2022	DRN BY DATE: KJB 3/24/2022 CHK BY DATE: MS 3/28/2022 APP BY DATE: HCR 5/9/2022 UNLESS OTHERWISE SPECIFIED ALL DIMENSIONS FOLLOW THE ANSI SYSTEM IPS (INCHES-POUNDS & SECONDS) TOLERANCES ARE: FRAC.1015 DEC: 4015, ang ±25°	
	8	7		6		5	<b>†</b>	ANY REPRODUCTION, PARTIA DOCUMENT WITHOUT THE W HINDLE POWER, INC IS S	RITTEN PERMISSION OF TRICTLY PROHIBITED.	3 3	

